

**Notice of References Cited**

Application/Control No.

09/838,084

Applicant(s)/Patent Under

Reexamination

LO ET AL.

Examiner

Suk-San Foong

Art Unit

2823

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,159,823	12-2000	Song et al.	438/437
	C	US-			
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Wolf S. "Silicon Processing for the VLSI-ERA: Volume 1-Process Technology", 1986, Lattice Pr., Vol. 1, p. 407, 476
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.